

ROAD COMMISSION FOR OAKLAND COUNTY

SPECIAL PROVISION  
FOR  
**DIGITAL TRAFFIC SIGNAL CONTROLLER**

RCOC/TOC:JJ

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**a. Description**

This special provision outlines the Road Commission for Oakland County's specifications for the traffic signal controller. The traffic signal controller shall meet or exceed all requirements of the NEMA Standard TS2-2003 specification, except as detailed below. This specification describes a digital type traffic signal controller meeting the latest NEMA specifications, as well as providing advanced features for future requirements.

**b. Materials**

**1. Traffic Signal Controller Unit (CU)**

This specification sets forth the minimum requirements for a shelf-mounted, two (2) through sixteen (16) phase, fully-actuated, digital, solid-state traffic controller. The controller shall be configurable to meet, at a minimum, all applicable sections of the NEMA Standards Publications for TS2 and NTCIP 1202 and ATC standard 6.10.

Where differences occur, these specifications shall govern. Controller versions shall be available to comply with NEMA TS2 Types 1 and 2. Type 2 versions of the controller shall be capable of operating as a Type 1 controller.

**A. Enclosure**

The controller shall be compact as to fit in limited cabinet space. It shall require no more than 7" shelf depth. External dimensions shall not be larger than 8.5" x 15.2 1/4" x 6.375" (H x W x D).

The top and bottom of the chassis shall be made from extruded aluminum and include an integral handle on the back for easy transport

The sides shall be constructed of injection molded polycarbonate.

The front panel shall meet specifications set forth in Section 2.5 (Front Panel)

The model, serial number, and program information shall be displayed on the outside of the controller.

**B. Electronics**

The electronics shall be modular in design and shall consist of vertical circuit boards. Horizontal circuit boards shall not be acceptable.

In the interest of reliability, no sockets shall be used for any electronic device. All devices shall be directly soldered to the printed circuit board. Surface mount parts shall be used for the majority of the electronic components in the controller.

A built-in, high-efficiency switching power supply shall generate the primary, +5VDC internal voltage, an isolated +24 VDC for internal and external use, VSTANDBY, LINESYNC, POWERUP and POWERDOWN signals. All voltages shall be regulated.

The 120 or 220VAC fuse shall be mounted on the front of the controller.

Protection for the 24VDC supply shall be provided by a resettable electronic fuse.

All printed circuit boards shall meet the requirements of the NEMA Standard plus the following requirements to enhance reliability:

- (1) Both sides of the printed circuit board shall be covered with a solder mask material.
- (2) The circuit reference designation for all components and the polarity of all polarized capacitors and two-leaded diodes shall be clearly marked adjacent to the component. Pin 1 for all integrated circuit packages shall be designated on all printed circuit boards.
- (3) All printed circuit board assemblies shall be coated on both sides with a clear moisture-proof and fungus-proof sealant.

Timing of the controller traffic application shall be derived from the AC power line.

To facilitate the transfer of user-programmed data from one controller to another, a data-key receptacle for using a separate 2070-style, serial flash memory device shall be an available hardware option. In addition, two USB sockets and one SD Card socket shall be provided for memory devices that can be used for data transfer. These data transfer devices shall be easily removable and directly accessible from the outside of the controller. The controller will not require this data-key, USB memory thumb drive, or SD Card to be present for proper operation.

All controller software shall be stored in Flash Memory devices. The controller software shall be easily updated without the removal of any memory device from the controller. The use of removable PROMS or EPROMS from the controller shall not be acceptable. The controller shall include an option that allows updating software using a Windows based computer, a USB memory thumb drive, or an SD card.

### C. ATC Engine Board

The controller shall include an ATC engine board compliant to ATC standard 5.2b and proposed version 6.10.

The engine board shall include a PowerPC 83XX family processor with QUICC engine.

The engine board shall have a minimum of the following memory:

- (1) 128Mbytes of DDR2 DRAM memory used for application and OS program execution
- (2) 64 Mbytes of FLASH memory used for storage of OS Software and user applications
- (3) 2MB of SRAM memory used for non-volatile parameter storage

The engine board shall provide the seven ATC serial ports, Ethernet, USB and all other control signal required by ATC standard.

The operating system shall be Linux 2.6.35 or later

### D. Graphical User Interface

The controller shall include an advanced graphics card. The graphics card shall enable:

- (1) Graphical display of status and programming selections.
- (2) The status displays shall include direction arrows for each of the phases and overlaps.
- (3) The selection of programming sections menus shall be by use of icons.
- (4) Programming shall use touch data entry, allowing touch gestures to select yes/no, select enable/disable, pull-down list selections, and more.

Touch selection of status and programming

Swiping to advance from one screen to the next

Program values shall be entered through the keypad. The user interface is a basic display with text and textual menus only (no touch).

The controller shall allow connection of a tablet type computing device to the controller.

(1) The tablet connection shall be by Wi-Fi connection either by a Wi-Fi to USB adapter or an Ethernet to Wi-Fi adapter.

(2) Once connected it shall be possible to observe the exact same graphics displays as are visible on the touch screen of the controller.

(3) It shall be possible to change the controller's programming values from the tablet while it is connected.

#### E. Front Panel

The front of the controller shall consist of a panel for the display, keyboard and connectors for all necessary user connections.

The display shall be a seven-inch (7"), color, TFT (Thin Film Transistor) LCD (Liquid Crystal Display) with high brightness. It shall be readable in direct sunlight. The display shall perform over the NEMA temperature range and shall have a resolution of 800 X 480 with an 18-bit color depth. The luminous intensity shall be a minimum of 800 nits. The display shall include an industrial, resistive touch screen that can be operated with gloved hands. The touch screen and display shall not be affected by condensation or water drops.

Front-panel operator inputs shall be via touch screen or by clearly labeled elastomeric keypad. These shall include a 10-digit numeric keypad, Main and Sub keys, toggle keys, special function and enter keys, six function keys, status and help keys and a large four-direction cursor control key.

The front panel shall include a built-in speaker for enhanced controller audio feedback.

The front panel shall include a tri-color status LED.

#### F. Ethernet Ports

The controller shall have the capability of supporting Ethernet communications, using TCP/IP communications protocols.

The controller shall provide four front-panel Ethernet ports.

Two of the ports shall be connected to Ethernet switch ENET1 and the other two shall be connected to Ethernet switch ENET2.

#### G. USB Ports

The controller shall provide two USB 2.0 ports.

USB ports shall be used for USB thumb drives to update software, upload or download configuration, or uploading logged data.

#### H. Connectors

All non-optional interface connectors shall be accessible from the front of the controller in the NEMA Configured Controller models. Configurations shall be offered to accommodate different versions, as follows:

- (1) NEMA TS2 Type 1
- (2) NEMA TS2 Type 2
- (3) NEMA TS1

The D connector shall be compatible with the Econolite Model *ASC/2*, *ASC/2S*, and *ASC/3* D connectors.

To facilitate special applications the controller shall have the capability of assignment of any input or output function to any input or output pin respectively on the interface connectors, with the exception of Flashing Monitor, Controller Voltage Monitor, AC+, AC-, Chassis Ground, 24VDC, Logic Ground and TS2 Mode bits.

The controller shall as a minimum have the following communications ports:

- (1) Port 1 SDLC for communications to other devices in the cabinet
- (2) Port 2 serial port for systems communications
- (3) Console serial port for local communications
- (4) Ports on ATC-2070 communication slots – see 2. Electronics
- (5) An optional telemetry module shall utilize TDM/FSK data transmission at 1200 baud or 9600 baud over two pairs of wires. This module shall include the Econolite 25-pin D-sub connector.

Serial communications shall operate at from 1200 to 115.2 K baud.

#### I. Serviceability

All electronic modules including the power supply shall be easily removable from the controller using a screwdriver as the only tool. All power and signal connections to the circuit boards shall be via plug-in connectors.

## J. Hardware Options

### Optional Data Key

- (1) A data-key and receptacle shall be available for use as a database storage device (backup) or as a database transfer module. It shall be capable of storing a minimum 2MB of data.
- (2) The data-key shall be hot swappable, so that it can be inserted and removed without powering down the controller.
- (3) The data-key shall be capable of storing the entire controller database and shall retain the information without use of battery or capacitor backup.
- (4) The controller shall not require this key to be present during normal operation.
- (5) If the data-key is present the controller shall automatically backup the database to the data key 20 minutes following the last data change.

### ATC-2070 type communications slot

- (1) The controller shall provide one ATC-2070 Type communications slot.
- (2) The slot will allow ATC 2070 type modules to be inserted.

## 2. Traffic Signal Controller Unit (CU) Software

This specification sets forth the minimum requirements for software that operates a two (2) through 16 (sixteen) phase, fully-actuated, digital, solid-state traffic controller. The controller shall meet, as a minimum, all applicable sections of the NEMA Standards Publications for TS2 and NTCIP 1202 and ATC standard 6.10. Where differences occur, these specifications shall govern. Controller versions shall be available to comply with NEMA TS2 Types 1 and 2. Type 2 versions of the controller shall be capable of operating as a Type 1 controller.

### A. Displays

#### (1) Dynamic Displays

Dynamic displays listed below shall be provided to show the operational status of the controller. Additional displays shall be offered for programming. It shall be possible to jump from a status screen to the appropriate programming screen and return after viewing is complete. When the cursor is moved to a dynamic location on the screen, an audible tone shall be provided when the display changes. When the cursor is moved around the status screen and the help key is pressed, a context sensitive help shall be displayed. The intersection, controller, coordination, preempt, and time-of-day (TOD) status displays shall contain a title line that displays the status being viewed, the controller status, the current date, and time.

**INTERSECTION STATUS DISPLAY** shall indicate a summary of ring, phase, coordination, preemption, TSP, communications, logic processor flags, and time-based control status. It shall be possible to place vehicle, pedestrian, preemption, and TSP calls from the keyboard while displaying status information.

**CONTROLLER STATUS DISPLAY** shall indicate current interval, pedestrian, density, maximum, and maximum extension timing by phase and ring. The status of vehicle, pedestrian, and overlap signal outputs shall be displayed in combination with vehicle and pedestrian calls. The display shall also show the split plan, timing plan, sequence, action plan, and day plan in effect. The display shall include the status of the first sixteen logic processor flags.

**COORDINATOR STATUS DISPLAY** shall indicate the status of vehicle signal outputs in combination with vehicle and pedestrian calls. The display shall also show the split plan, timing plan, sequence, action plan and day plan in effect. Also displayed shall be, current coordination pattern information, local and system cycle count, time-based control status, hold, force-off, vehicle permissive, pedestrian permissive, split count down, split extension, and offset from ring 1.

**PREEMPTOR STATUS DISPLAY** shall indicate the status of vehicle, pedestrian overlap and overlap signal outputs shall be displayed in combination with vehicle and pedestrian calls. Priority (railroad, fire, emergency) preemptors and bus preemptors with calls, preemptor active, inhibit, and delay status. When a preemptor is active, the display shall also indicate preemptor interval, timing, duration, and dwell status. A portion of the display shall indicate the controller status during preemption including current status, interval, and timing by phase and ring and the status of vehicle and pedestrian signals for each phase.

**TIME BASE STATUS** display shall indicate the current time and date the source of pattern selection, the next day plan to be selected, the current schedule, day of the week, day plan number, day plan number, action plan number and the action plan start time. This status screen shall display a total of 30 TOD events.

**COMMUNICATION STATUS** displays shall be communications status displays for Ethernet, Port 1 (SDLC), Port 2 (terminal) Port 3, and NTCIP.

- (a) An Ethernet status display shall indicate the line speed, the line status, the total number of transmits and receive counts and the number of transmit and receive error counts.

(b) Port 1 (SDLC) status display shall indicate the frame responses from the MMU, the terminal and facilities BIUs and the detector BIUs.

(c) Ports 2 and 3 status display shall indicate the interconnect format, transmit, valid data, data error, carrier detect and the last valid command.

(d) An NTCIP status display shall indicate the total number of SNMP and STMP transmit and receive counts.

**DETECTOR STATUS** display shall indicate activity for up to 64 detectors. The display shall show detector calls as they are processed by the controller. The display shall also show the extension and delay timers for the selected detector. In addition, the failure status of the detector shall be displayed.

**FLASH/MALFUNCTION MANAGEMENT UNIT (MMU)** status display shall indicate flash status plus MMU channel, conflict, and monitoring function status. A separate display shall indicate the results of the controller's comparison of its MMU programming to the programming in the controller.

**INPUT AND OUTPUT STATUS** displays shall indicate the activity of all the logic level inputs and outputs to the controller.

#### B. Programming Displays

Programming displays in the form of menus shall aid the operator in entering data from the front-panel keyboard.

A main menu shall allow the user to select a major function of the controller. A submenu shall then be displayed to allow the user to select a sub-function within the major function.

English language and traffic engineering terminology shall be used throughout to facilitate programming. The display organization shall allow traffic personnel to program the controller without using reference cards or manuals. All data entry and data screens shall be in logical order.

Four (4) arrow cursor key shall allow the user to scroll through all programmed data.

Programming entries shall consist of alpha-numerical values, YES/NO and ON/OFF entries. During program entry, the new data shall be displayed as it is entered. Warning and consistency checks shall be performed when ENTER or cursor key is pressed. On constrained data (entries that are constrained by other programmed data), entries shall only be stored when the consistency check is validated. On non-constrained data, the entries shall be stored only when the new values are within valid value thresholds.

(1) An example of constrained data is the sequence of the phases within a ring. They need to be checked with the phase compatibility, phases in the ring and start phases among others.

(2) An example of non-constrained data is the vehicular extension time entry.

The keyboard entry software shall include context sensitive help screens. Help information shall be accessed by placing the cursor on the data entry in question then pressing the HELP key. Help screens shall be provided for all keyboard-entered data and shall include at a minimum range, description, and functional operation information for the data entry.

### C. Programing

#### (1) Programming Methods

The methods listed below shall be available for controller configuration and timing entries. The manufacturer shall be able to provide as off-the-shelf items all the firmware and software required to affect the listed methods and to implement network operation with system masters and host PC's.

(a) Manual data entry via the front panel keyboard.

(b) Downloading via telemetry from a system master connected to a host PC in a closed-loop system or a central server in an enterprise system.

(c) Downloading from a portable PC-compatible computer via a serial or Ethernet cable.

(d) Transfer from one controller to another, or restoring for a back-up copy, using a data key, USB thumb drive, or an SD card.

(e) A PC web interface shall allow the operator to view the current status and programming for the controller. It shall be possible to change programming from a remote device like a PC and if the device is connected to the controller, the programming changes shall take effect the next time that interval or function is used.

#### (2) Programming Security

A minimum of three access levels shall be available to provide programming security.

(a) The highest or administrator level shall have access to all programming entries including setting access codes.

(b) The second or data change level shall have read-write privileges for all programming entries except access codes and CRC enable/disable.

(c) The third or data display level shall only have access to read programmed data and status screen.

User selectable, access codes shall be provided for the administrator and data change and read only access levels. Each user shall have a user name that is entered using from 6 to 15 characters. Access codes shall initially be set to provide unrestricted access.

If there has been no keyboard activity the controller shall automatically logoff the user after 30 minutes.

### (3) Programming Utility Functions

A copy function shall permit copying all timing data from one phase to another. It shall also permit copying all timing plans from one timing plan to another, one detector plan and detector options plan to another, all coordination pattern data from one pattern to another and one sequence to another. This feature will facilitate data entry when programming any two or more phases with the same timing values, or detectors with the same programming, and/or two or more coordination patterns with the same pattern data.

The controller unit shall contain a backup database with user specified values stored in non-volatile memory. A copy function shall permit transferring the backup database to the active database. The user shall be able to create their database and copy it to the default database.

A sign-on message shall allow the user to view the controller software version number. It shall also be possible to display the sign-on message by keyboard selection. The sign-on display shall allow a user-defined message of up to two lines with 38 characters per line.

The controller shall have the capability to output a memory image of the user programmed settings and intersection configuration data in binary format.

This shall allow transferring the memory image data to a data key, USB thumb drive, or a SD card.

## D. Actuated Control Functions

The controller software shall provide all actuated control functions and operations required by the NEMA TS2 Standard. In addition, it shall provide the features described in the following sub sections.

### (1) Phase Sequence

The phase sequence of the controller shall be programmable in any combination of sixteen phases, using up to 16 concurrent groups and four timing rings.

Up to 16 unique phase sequences shall be user configurable and used in the controller.

Four additional fixed sequences shall conform to the following TxDOT "Diamond" configurations:

- (a) 3-phase diamond
- (b) 4-phase diamond
- (c) NEMA 8-phase diamond
- (d) 2 intersection diamond control

Phase sequence information shall be changeable from the keyboard and stored in data memory.

The standard phase sequence of the controller shall also be capable of being altered by coordination, TOD or external alternate sequence command, by selection 1 of the 16 configurable sequences or 1 of the 4 "diamond" sequences.

An exclusive pedestrian phase feature shall be provided which will time and display the pedestrian indications with the vehicle movements remaining in all red.

#### (2) Timing Intervals

Timing intervals shall be programmable from 0-255 in one second increments or from 0-25.5 in one-tenth second increments, depending on the function.

Four independent timing plans shall be provided. Any plan shall be selectable on a TOD basis, by coordination pattern, or for one cycle following preemption.

Each timing plan shall contain the following interval timings:

Minimum Green	Maximum 3
Bike Green	Dynamic Maximum
Delay Green	Dynamic Maximum Step
Conditional Service Minimum Green	Yellow Clearance
Walk	Red Clearance
Walk 2	Red Maximum
Walk Maximum	Red Revert
Pedestrian Clearance	Actuations before Reduction
Pedestrian Clearance 2	Seconds per Actuation
Pedestrian Clearance Maximum	Maximum Initial
Pedestrian Carryover	Time before Reduction
Vehicle Extension	Cars Waiting

Vehicle Extension 2	Time to Reduce
Maximum 1	Min Gap
Maximum 2	Steps to Reduce

The bike green interval shall replace the phase minimum green if the interval time is larger than the min green time and if a detector input designated as a bike detector has been activated.

Two Walk and Pedestrian Clearance intervals shall be provided for each phase per timing plan. The second Walk and Pedestrian Clearance shall be activated by a time base action plan.

Two vehicle extension intervals shall be provided for each phase per timing plan. The active vehicle extension interval shall be selected by a time base action plan.

If enabled, a Delay Green timer shall delay the vehicle phase from starting until the timer has expired. This shall provide an additional all red for the vehicles movement and allow the pedestrian indications to precede the vehicle movement.

The Pedestrian Walk interval shall extend from Walk to the smaller of the Walk Max time or the phase maximum in effect with a constant input from the "Walk Extension detector". The Pedestrian Clearance interval shall also extend Pedestrian Clearance to the smaller of the Pedestrian Clearance Max time or the phase maximum in effect with a constant input from the "Walk Extension detector."

Volume density intervals shall include actuations before adding to the initial green and the number of cars waiting while the phase is not green. Actuations before added shall provide a user-specified number of actuations that must occur before adding variable (added) initial time. Cars waiting shall record the number of cars recorded while the phase is not green, the start of gap reduction is initiated by the time before reduction or the cars waiting which ever reaches its programmed value first.

The controller shall be capable of dynamically extending the maximum green time for each phase based on vehicle demand. Three maximum green intervals shall be selectable per phase based on TOD, coordination pattern, or external input. The initial interval shall be selectable as Max 1, Max 2, or Max 3. If the phase terminates due to max-out for two successive cycles, then the maximum green time in effect shall automatically be extended by a dynamic max step interval on each successive cycle until it is equal to dynamic maximum. If the phase gaps out for two successive cycles, then the maximum green time shall be reduced by the dynamic max step time until it reaches to the original max value.

Each phase shall have a red maximum timing interval. An input (red extension) shall extend the all red period of the assigned phase as long as the detector input is true. This input must be true within the all red time of the assigned phase to be able to extend the all red period. If this detector fails then the all red extension feature shall be disabled.

Guaranteed minimum interval values shall be settable and shall not be overridden by the controller. Values shall be provided for the following intervals:

- (a) Minimum Green
- (b) Walk
- (c) Pedestrian Clearance
- (d) Yellow Clearance
- (e) Red Clearance
- (f) Overlap Green
- (g) Overlaps

The controller shall provide 16 (sixteen) internally-generated overlaps (A - P). All overlap functions shall be programmable from the controller keyboard.

Overlaps shall be individually programmable as standard (normal), other (to include minus green/yellow, protected, pedestrian protected, and protected permissive flashing arrow), with lag overlap timing capabilities. The green, yellow, red and lag interval timings shall be individually programmable with respect to the activation or termination of the parent phase, respectively.

The standard overlap shall require only included phases to be configured.

A protected overlap shall operate according to the following rules:

- i) Overlap shall be green, yellow, or red like a normal overlap except its outputs shall be blank when the protected phase is green, or the controller is transitioning to a non-included phase.

Minus green/yellow overlaps shall operate according to the following rules:

i) Overlap shall indicate green when any of the overlap phases are green or when in transition between overlapped phases and a modifier phase is not green.

(h) The overlap shall be yellow when an overlapped phase is yellow and the modifier phase is not yellow and none of the overlapped phases are next.

A pedestrian protected overlap shall be green under the following conditions:

i) When an included phase is green and the protected pedestrian is NOT in walk or pedestrian clearance.

ii) When the controller is in transition between included phases and a pedestrian protected phase is not next.

iii) After servicing an included phase pedestrian demand if there is enough time before max out to service the overlap minimum green.

The controller shall provide an overlap selection for Protected/Permissive Left Turn (PPLT) Flashing Yellow Arrow FYA operation. PPLT/FYA shall allow selection of the protected phase, permissive phase, flash yellow output to either the overlap output, or a pedestrian clearance (yellow) output. The PPLT/FYA shall also allow the yellow arrow to continue flashing once the permissive through phase reaches yellow clearance. PPLT/FYA shall be enabled on a TOD basis.

The controller shall provide the capability of sixteen pedestrian overlaps. These shall be capable of overlapping the pedestrian displays of any combination of phases with a pedestrian movement.

#### E. Conditional Service

The controller shall provide a programmable conditional service feature. When selected, the controller shall service an odd-numbered phase once normal service to that phase has been completed and enough time for additional service exists on the concurrent even phase.

A conditional service minimum green time shall be programmable for each phase. This interval shall ensure a minimum green if the phase is conditionally served.

It shall be possible to program the controller to re-service the even phase after conditionally serving an odd phase. Once an even phase has been conditionally re-served, the odd phase shall not be conditionally served again until returning to the concurrent group that is timing.

#### F. Additional Features

The following features shall be programmable for each phase in each of four separate timing plans:

- Locking/non-locking detector memory
- Vehicle Recall
- Pedestrian Recall
- Maximum Recall
- Soft Recall
- No-Rest Phase
- Enable Added Initial

Also programmed by phase shall be:

- Phase in Use
- Exclusive Pedestrian Phase

Soft recall shall return the controller to the programmed phase in the absence of other calls.

If a phase is designated as a no-rest phase the controller shall not rest in the phase, and proceed to the next phase with serviceable demand, or if no demand is present the next soft recalled phase.

The controller shall permit power start and external start to be programmed by phase and interval. Start intervals shall be green, yellow red, or yellow with overlaps forced yellow.

During a power start condition, the controller shall be capable of timing an all-red or flash interval before the power start phase(s) and interval(s) are displayed. The controller shall support the MUTCD 2009 that requires a minimum of 6 seconds of all red following red/red flash.

The controller shall provide guaranteed passage operation on a per phase basis. When selected, this feature shall provide a full passage (vehicle extension) interval when a phase gaps out with a gap in effect less than the vehicle extension interval (preset gap).

The controller shall provide both single and dual entry operation. When selected, dual entry shall cause the controller to ensure that one phase is timing in each ring.

It shall be possible via keyboard selection to inhibit the service of a phase with other phase(s) within the same concurrent group.

The controller shall provide the following additional selectable pedestrian functions:

- Actuated phase rest in WALK

- Flashing WALK output

- Pedestrian clearance protection during manual control

- Pedestrian clearance through yellow

- Pedestrian timing shall be capable of being carried over from one phase to another

Programming shall be provided to inhibit re-service of odd phases (left turns) within the same concurrent group. Also, programming shall be provided to place a demand on a phase in another concurrent group to cause the controller to leave the concurrent group prior to servicing the odd phases in the same concurrent group.

The controller shall provide a programmable simultaneous gap termination feature. When programmed, phases in both rings shall gap out together to terminate the green interval and cross the barrier.

The controller shall provide automatic flash selection per the requirements of the MUTCD. Both the flash entrance and exit phases shall be programmable through the keyboard, and flashing shall be controlled by either setting the fault/voltage monitor output to be FALSE or by flashing through the load switch driver outputs.

If flash desired through the load switches, both the phase and overlap outputs shall be flashed either yellow or red as selected by the operator. Automatic flash shall be selectable by external input, system command, or TOD action plan.

The controller shall provide dimming for selectable load switch outputs. Dimming shall be accomplished by inhibiting the selected outputs for alternate half cycles of the 120 VAC line. Dimming shall be controllable by TOD and an external input; both functions must be TRUE for dimming to occur. Programming shall permit individual dimming of the Green/Walk, Yellow/Ped Clear, Red/Don't Walk outputs for each load switch. Based upon controller programming the controller shall automatically compute the MMU channel compatibility. The compatibility computed shall be based on phases-in-use, phase concurrency, valid pedestrian movements, vehicle and pedestrian overlaps, and pedestrian carryover.

#### G. Coordination

Coordination functions to control intersection cycle lengths, system offset relationships, and phase split percentages shall be provided as a standard feature, with no need for additional modules or software.

##### (1) Coordination Patterns

A minimum of 120 coordination patterns shall be provided. Each pattern shall allow selection of an independent cycle length, offset value, and split pattern.

The coordination patterns shall be selected using telemetry (system), hardwire, or non-interconnected (time-base) coordination commands. Offset and Split values shall be entered in either seconds or percentages.

The coordination patterns shall be selected by the coordination command using the following formats:

(a) Pattern - This format shall allow selecting the coordination patterns directly, that is, commanding Plan 1 selects Pattern 1. Pattern command shall include 1-120 patterns, pattern 254 shall select free, and pattern 255 shall select flash.

(b) Standard - This format shall allow selecting the coordination patterns using a pattern number derived from a cycle offset-split command. Each pattern shall be assignable to a specific cycle-offset-split combination. The coordination pattern shall be selected programmed by the user to each pattern used.

(c) TS2 - This format shall allow selecting the coordination patterns as a function of Timing Plan and one of three offsets. With this format, a minimum of 20 Timing Plans shall be available for selection of one of sixty coordination patterns.

The following functions shall be programmable in each coordination pattern:

- i) Cycle length
- ii) Split pattern number
- iii) Offset value
- iv) Dwell/add time
- v) Actuated coordination
- vi) Timing plan
- vii) Actuated walk rest
- viii) Phase sequence
- ix) Phase re-service
- x) Action plan
- xi) Maximum select

- xii) Fixed/Floating force off
- xiii) Split timing per phase in seconds of percentage
- xiv) Directed split preferences
- xv) Coordinated phase split extension
- xvi) Crossing artery pattern
- xvii) Permissive timing
- xviii) Ring extension
- xix) Split demand pattern
- xx) Ring displacement
- xxi) Coordinated phase
- xxii) Split value by phase
- xxiii) Omit by phase
- xxiv) Min recall by phase
- xxv) Max recall by phase
- xxvi) Pedestrian recall by phase
- xxvii) Special function outputs

The following functions shall be programmable for each of the 120 Split patterns:

- i) Coordinated phase
- ii) Split value by phase
- iii) Omit by phase
- iv) Min recall by phase
- v) Max recall by phase

vi) Pedestrian recall by phase

vii) Max and Pedestrian recall by phase

#### H. Cycle Length

One cycle length shall be provided for each coordination pattern. The cycle shall be adjustable over a range of 30-999 seconds in 1-second increments.

The cycle length shall serve as the reference time for all coordination timing.

#### I. Synchronization

For systems with a single system sync pulse, coordination timing shall be synchronized to the leading edge of that pulse, which shall serve as the master zero reference for all offset timing.

For hardwire systems with multiple sync pulses, the coordinator shall lock onto the correct sync by trying different syncs and checking for reoccurrence during successive cycles.

After a valid system sync pulse has been received the coordinator shall check for the proper occurrence of the system sync pulse during each subsequent cycle. If a sync pulse does not occur, the coordinator shall self-sync and continue to operate with the last set of coordination commands for a programmable number of cycles from 0-255. If a sync pulse does not occur within the programmed period (or until the first sync pulse is received), the coordinator shall revert to the non-interconnected coordination mode.

#### J. Offset

Offset shall normally be defined as the time period from the system sync pulse to the beginning of the leading coordinated phase green (local zero). The coordinator shall also be capable of referencing the offset to the beginning of the lagging coordinated phase green, coordinated phase yield, start of yellow point or the start of the ring 1 coordinated phase.

Offsets shall be programmable using both percent and seconds. The range shall be from 0-99% of the cycle length in 1% increments or 0-255 seconds in 1-second increments.

Offset changes shall be achieved by adding or subtracting cycle time over a maximum of three cycle periods to allow a smooth transition to the new offset.

Other offset change methods shall be adding 20% or a user-selectable value to each cycle or to snap to the sync point once the permissive period are complete and the coordinated phases are green. Offset correction using dwell shall also be selectable.

#### K. Split

Each split shall provide a split interval for each of sixteen phases. The split interval shall be programmable using percent or seconds. The range shall be from 0-99% of the cycle length in 1% increments or 0-255 seconds in 1-second increments.

Split interval settings shall determine the maximum time, including vehicle clearance (yellow and red), for a non-coordinated phase, or the minimum time for a coordinated phase. Phase termination shall be controlled by establishing a force-off point for each phase within the cycle. Except for the coordinated phases the force-off point shall be selectable to be a fixed point within the cycle or allowed to float. If floating force-offs are selected each phase shall time no more than its own split interval.

During coordination, it shall be possible to operate a coordinated phase as actuated or non-actuated. If a coordinated phase is actuated, vehicle detections shall permit the coordinator to extend a phase beyond the normal yield point.

Extended coordinated phase green shall be selectable using the same range as split interval settings (percent or seconds). If actuated coordinated phases are used they shall be able to have actuated or non-actuated (walk rest) pedestrian movements.

#### L. Permissive Periods

Permissive periods shall be provided to control the time period during which coordinated phases are released to service calls on non-coordinated phases. All permissive timing shall begin at the lead coordinated phase yield point. A yield point shall be automatically computed for the coordinated phase in each ring.

The coordinated phase yield points shall allow the coordinated phases to yield independent of each other. The yield point shall be the point at which the coordinated phase is released to allow the controller to service calls on non-coordinated phases. The computation shall take into account the coordinated phase split interval plus pedestrian and vehicle clearance times.

Automatic permissive period operation shall be provided by automatically calculating a permissive period for each non-coordinated phase. The permissive period shall consist of a separate vehicle and pedestrian period computed from the phase split interval and the vehicle/pedestrian minimum time. The controller shall answer a call only during the associated phase permissive period. However, once the controller has been released to answer a call, all remaining phases shall be served in normal sequence.

Single permissive period operation shall be provided by defining a single time period per cycle beginning with the yield point during which the controller is allowed to answer phase calls for any phase. The duration of this period shall be selectable in each coordination pattern.

Dual-permissive period operation shall also be provided. During the first permissive period, the controller shall answer only vehicle or pedestrian calls on the phases following the coordinated phase. If the controller yields to a call during this period, calls on the remaining phases are served in normal rotation.

During the second permissive period, the controller shall answer calls on all remaining phases except the first permissive phase. The duration of the two permissive periods, and the time at which to start the second permissive period (displacement), shall be selectable in each coordination pattern.

#### M. Phase Re-service

If actuated coordinated phases are in use it shall be possible to re-service non-coordinated phases within the same cycle if sufficient time remains. A phase shall be re-serviced only if the permissive period for the phase indicates there is sufficient time remaining in the cycle to service the phase.

Phase re-service shall be capable of being enabled/ disabled in each coordination pattern.

#### N. Transition Cycles

It shall be possible to program the controller to ignore the pedestrian timing when calculating the minimum cycle length for offset correction.

The controller shall provide a smooth and orderly transition when changing from free operation to coordinated operation and from one coordination command to another.

During a free-to-coordinated transition, the controller shall initiate a pick-up cycle beginning upon receipt of a sync pulse and a valid coordination command. The controller shall then enter coordination mode upon crossing a barrier or if resting in the coordinated phases.

Each coordination command shall select a pattern. A command change shall be implemented concurrent with a sync pulse. Cycle, offset, and split changes shall not take effect until local zero.

#### O. Crossing Artery Control

The coordinator shall be capable of implementing dual coordination at an intersection where two arterials are under control of separate masters.

An external input shall enable dual coordination. Once enabled, the coordinator shall place a continuous call on the crossing artery phases so as to ensure that these remain green for their full split interval.

The coordinator shall output a crossing artery sync signal to indicate the beginning of the crossing artery phase split interval.

Dual coordination shall force a selectable crossing artery split plan to be used so as to allow a particular split to be optimized for dual coordination in each coordination pattern.

#### P. Local Split Demand

The coordinator shall provide a minimum of two split demand detector inputs, which shall allow the selection of a preferred split plan based on intersection demand.

If the split demand detector indicates continuous vehicle presence during a programmed monitoring period beginning with the onset of a selected phase green, the coordinator shall force a selectable split plan to be in effect during the next cycle. This split plan shall remain in effect for a selected number of cycles from 0-255. A specific split plan shall be capable of being selected in each coordination pattern.

#### Q. Adaptive Split Demand

The coordinator shall provide a method to select the split using measurement of each phase's green utilization. From the green utilization measurement, the coordinator shall determine which phase or phases had excess time that was not used during the last measurement period. Then the excess time shall be added to the first set of preferential phases. If the first set of preferential phases gapped out during the last measurement period, then the excess time will be added to a second set of preferential phases. If both sets of preferential phases gapped out during the last measurement period then the time shall be added to the beginning of the coordinated phases.

#### R. Free Mode

The coordinator shall provide a free mode of operation, where all coordination control is removed.

Free mode operation shall be selectable by coordination commands, by external input or by keyboard entry.

The coordinator shall revert to the free mode when active controller inputs or functions would interfere with coordination. Such inputs or functions shall include the following:

- Manual control enables
- Stop time
- Automatic flash
- Preemption

The coordinator shall provide an active free mode, where coordination control is removed but the coordinator continues to monitor system sync so as to keep its timing in step with the system master or time of day if operating under TOD or central system control.

#### S. Manual Control

The controller shall allow manual override of the current coordination command from the keyboard. The manual command shall allow selection of any coordination pattern to be in effect.

#### T. Interconnect Modes

The coordinator shall can operate with any of the following interconnect types:

- Non-interconnected coordination (time-based)
- Telemetry
- Hardwired

The coordinator shall be compatible with fixed-time interconnect, which provides the sync pulse superimposed on the offset lines. It shall also operate within an interconnected system using a separate sync line. The non-interconnected coordination mode shall serve as a backup when using telemetry or hardwired interconnect.

#### U. Preemption

The controller shall provide a minimum of ten preemption sequences that can be programmed as either railroad-fire-emergency or bus vehicle preemption sequences. Preemption capability shall be standard and shall not require additional modules or software.

##### (1) Railroad-Fire-Emergency Vehicle Preemption

The ten railroad-fire-emergency vehicle preemptors shall be selectable as a priority or non-priority type. Priority preemptor calls shall override non-priority preemptor calls. Low-numbered priority preemptors shall override higher-numbered priority preemptor calls. Non-priority preemptor calls shall be serviced in the order received.

Each preemptor shall provide a locking and non-locking memory feature for preemptor calls. If a preemptor is in the non-locking mode and a call is received and dropped during the delay time, the preemptor shall not be serviced.

Preemptor timing intervals shall be programmable from 0-255 in one-second increments or 0-25.5 in one-tenth second increments, depending on function. Delay, max presence, and duration timing intervals shall be programmed from 0 – 65535 seconds in one-second increments.

A programmable delay time interval shall be provided to inhibit the start of the preemption sequence. This interval shall begin timing upon receipt of a preemption call. This time shall be programmable from 0-65535 seconds in one-second increments.

An inhibit time shall be provided as the last portion of the delay time interval. During this time, phases that are not part of the preempt sequence shall be inhibited from service. This time shall be programmable from 0-255 seconds in one-second increments.

A programming option shall be available that allows termination of all phases prior to entering preemption.

A programmable extend input shall cause the preemptor to remain in the dwell interval following the removal of the preempt call. If a preempt call is reapplied during this time, the preemptor shall revert to start of dwell interval. This time shall be programmable from 0-25.5 seconds in one-tenth second increments.

A programmable duration time shall be provided to control the minimum time that a preemptor remains active. This time shall be programmable from 0-65535 seconds in one-second increments.

A programmable maximum presence time shall be provided to control the maximum time that a preemptor input remains active and still be recognized by the controller. Once failed, the input must return to inactive state to be recognized again. This time shall be programmable from 0-65535 seconds in one-second increments.

Phases timing at the beginning of a preemption sequence shall remain in effect for a minimum time before the controller advances to the next sequential interval. If the phase has been timing for longer than the programmed preemptor minimum time, the controller shall immediately advance to the next sequential interval. Minimum times shall be programmable for the following intervals:

- Green/walk/pedestrian clearance
- Yellow
- Red

A phase shall advance immediately to pedestrian clearance if it has been timing a WALK interval at the beginning of a preemption sequence. It shall be possible to time the minimum pedestrian clearance through the yellow interval, or alternately to advance immediately to yellow. During preemption, pedestrian indicators shall be selectable as being a solid DONT WALK, OFF (blank), or fully operational.

If an overlap is in effect when the preemption sequence begins, it shall be possible to terminate the overlap so that it remains red for the remainder of the preemption sequence. Overlaps terminating or forced to terminate shall time the preemptor minimum yellow and red clearance times.

Each preemptor shall provide user-programmable green, yellow, and red track clearance intervals. These shall begin timing immediately after the preemptor minimum red interval.

Each preemptor shall provide a user-programmable gate down extension and gate down max green. The gate down extension shall extend the track clearance green time after the gate down input is received. The gate down max green is the maximum the track clearance green time will be extended. If the gate down max green is exceeded the intersection shall be forced to flash.

The forced flash shall be programmable to be either hard or soft. If hard flash is enabled then exiting flash to normal operation shall require either pressing the clear key or the MMU reset. A programmed soft flash shall automatically return to normal once the fault condition no longer exists. For gate down fault this shall be once preemption is deactivated.

Up to four permissive phases shall be selectable as track clearance phases. During the track clearance period, the selected phases shall time the track clearance green, yellow and red intervals once, and then advance to the hold interval. If track clearance phases are not selected the track clearance interval shall be omitted from the preempt sequence. Controller interval timing shall be used if track clearance interval times have been programmed as zero.

The preemption hold interval shall begin immediately after track clearance. It shall remain in effect until the preemptor duration time and minimum hold times have elapsed and the preemptor call has been removed or the preemptor maximum time has been exceeded. During the preemption hold interval, any one of the following conditions shall be selectable:

- Hold phase green
- Limited phase service
- All red
- Flash

Any valid phase, except a track clearance phase, shall be selectable as a hold phase. If hold phases are not selected, the controller shall remain in all red during the hold interval. If flash is selected for the hold interval, up to two permissive phases shall be selectable to flash yellow, and the remaining phases shall flash red. Overlaps associated with the phases flashing yellow shall also flash yellow unless they have been forced to terminate, in which case they shall remain red.

The preemptor shall immediately cause flashing operation if the preemption input and the track interlock input are not in opposite states and the track interlock function is enabled.

Each preemptor shall provide a user-programmable green, yellow, and red dwell interval, during which the dwell phase(s) shall operate normally, except that the minimum green interval time shall equal the hold green time. At the completion of the dwell green interval, the controller shall time the dwell yellow and red clearance intervals prior to transfer to the exit phases.

Up to four permissive exit phases shall be selectable to time after the preemption sequence has been completed. These shall serve as transition phases to return the controller to normal operation. It shall also be possible to place calls on selected phases upon exiting preemption. The option shall be provided to cause the preemptor to exit preemption to the correct phase to maintain coordination.

Each preemptor shall provide a user-selectable exit timing plan. Upon exiting the preemption sequence, this timing plan shall serve as the phase times in effect for one controller cycle for all phases.

Preemptor linking shall permit preemption sequences, where lower-priority preemptors may call the higher-priority preemptors from their preemption sequence.

Each preemptor shall allow programming of an inhibit extension timer. This timer shall be the length of time the max call inhibit can be extended.

Preemptor active outputs shall be provided for each of the preemptors. The output shall be set to ON when the preemption sequence begins and shall remain ON for the duration of the sequence. It shall also be possible to program preempt active outputs to be ON only during preempt hold intervals. Additionally, it shall be possible to program the non-active, non-priority preemptor outputs to flash while another preemptor is active.

Preemptors shall normally override automatic flash. It shall be possible to inhibit this feature for each preemptor.

Each preemptor shall provide the ability to delay the preempt input based on vehicle and pedestrian service request in relation to the coordinated cycle. The preemptor will compare the preempt delay time to the minimum time required to service a phase or pedestrian and then skip the phase or pedestrian movement if there is not enough time left in the delay to service the movement.

Each preemptor shall allow pedestrian movements which were interrupted by a preempt call to be dynamically selected as that Preemptor's exit phases.

Each preemptor shall allow vehicle phases which have been interrupted by a preempt call to be dynamically selected as that Preemptor's exit phases. The preemptor shall calculate the percentage of green time served of phases interrupted by a preempt call. If the amount serviced does not meet the limits defined by the Priority Return % data entries then the preemptor run shall select those interrupted movements as exit phases.

Each preemptor shall allow phases that have been waiting the longest to be serviced or which have the most cars waiting to be dynamically selected as that Preemptor's exit phases.

The conditional delay, interrupted pedestrian, interrupted vehicle and queue delay functions shall be programmed for enable, disable or time of day selection.

Each preemptor shall, if free one cycle is enabled, allow the preemptor to exit to free for one cycle before returning to coordination.

Each preemptor shall, if exit phase once is enabled, allow the preemptor to exit to specific phases in free mode (for a special minimum green time as defined in a new timing plan) before returning to coordination.

## (2) Bus Preemption

Ten bus preemptors shall provide control for bus or other low-priority vehicles. Bus preemptors shall have low priority and shall be overridden by railroad-fire-emergency vehicle preemptor calls.

The preemptor shall be programmed to accept either a 6.25 pulse-per-second signal with a 50% duty cycle or a solid input to identify a bus preemptor call. Bus preemptor calls shall be capable of preemptor call memory and shall be served in the order received.

Bus preemptor timing intervals shall be programmable from 0-255 in one-second increments or 0-25.5 in one-tenth second increments depending on the function.

A re-service time shall be provided to avoid excessive utilization of the same bus preemptor. If a call is received before the re-service time has elapsed, the bus preemptor shall not be re-serviced. If re-service time has not been entered then all phases with a call when leaving the bus preemption sequence shall be serviced before the bus preemptor may be served again.

Bus preemptors shall provide delay, inhibit, and maximum time functions similar to those for railroad-fire-emergency vehicle preemptors described above.

Bus preemptors shall provide the following entrance intervals:

- Green/walk/pedestrian clearance
- Yellow
- Red

At the completion of the entrance red clearance, the bus preemptor shall advance to the hold green interval. During this interval, up to four (4) permissive phases shall be selectable to remain green until the minimum hold time has elapsed and the bus preemptor call has been removed or the preemptor maximum time has been exceeded.

It shall be possible to program the controller to allow concurrent phases to be serviced for a bus preemptor with only one phase selected as the hold interval phase.

### (3) Preemption Safeguards

If a preemptor call is active when power is restored to a controller, the fault/voltage monitor output shall be set to FALSE, placing the intersection in flash. Similarly, if external start is applied during a preemption sequence, the intersection shall be set to flash. Intersection flash shall remain in effect until the preemptor call has been removed and the preemptor duration time has elapsed.

An input shall be provided to stop timing of the current active preemptor under control of the MMU/CMU.

A preemptor safety interlock shall be provided to cause the intersection to go into flash whenever the controller has been replaced and/or has not been programmed for preemption. This shall be achieved with an appropriate signal to the MMU/CMU.

### (4) Optional Transit Signal Priority (TSP)

The controller shall include a transit signal priority algorithm that provides for transit vehicle movement through the intersection, while not interrupting coordination or skipping phases.

A check-in detector input shall be provided that senses the arrival of the transit vehicle. When active this input shall initiate TSP.

A TSP delay shall delay the beginning of TSP operation until a set interval after check-in.

A check-out detector input shall determine the departure of the transit vehicle.

Assignment of a single pulse from the check-in detector and check-out detector to the controller inputs shall be programmable to any controller input.

When under coordination the TSP sequence shall use adjusted split times to accommodate transit vehicles while maintaining coordination.

When under free operation the TSP sequence shall use adjusted maximum times to accommodate transit vehicle while not skipping phase.

#### V. Time-Based Control (Non-Interconnected) Coordination

The controller shall include time-based control. This capability shall be a standard feature and shall not require additional modules or software.

##### (1) Clock/Calendar Functions

The controller shall provide a TOD clock, which shall be used for all time-based control functions. The only required clock settings shall be the current time (hour, minute, and second) and date (month, day, and year). Day of week and week of year shall be automatically computed from the date setting. It shall also be possible to set the number of hours that the local standard time is ahead or behind Greenwich Mean Time.

During normal operation, the TOD clock shall use the power line frequency as its time base. When power is removed, the time shall be maintained by a crystal oscillator for up to 30 days. The oscillator shall have a timing accuracy of +/- 0.005% over the entire NEMA temperature range as compared to the Universal Coordinated Time Standard.

In addition to entering time and date via the keyboard, it shall be possible to download the information from a central computer or a system master.

The controller shall include a time reset input. This feature shall reset the TOD clock to 03:30 whenever the time reset input is TRUE.

The controller shall provide support for a serial input from a GPS clock that supports NEMA protocol to set the time and date of the controller.

The TOD clock shall automatically compensate for leap year and shall be programmable to automatically switch to daylight savings time.

##### (2) Time-Based Control

Time-based control shall utilize a day plan program format. The month program shall consist of 200 programmable schedules, each assignable to one of 16 (sixteen) day programs. Each day program shall consist of from 1 to 50

program steps, which define a program for the entire day. Each program step shall be programmed with a starting time and an action plan number. The day plans shall also be assigned to days of the week and days of the month.

Time based control shall use action plans to assign:

- Coordination pattern number
- Vehicle detector plan number
- Controller sequence
- Timing plan
- Vehicle detector diagnostic plan
- Pedestrian detector diagnostic plan

Time based control shall also use action plans to enable:

- Automatic flash
- System override
- Detector log
- Dimming
- Special functions
- PPLT/FYA
- Priority return
- Ped priority return
- Queue Delay
- Conditional delay
- Auxiliary functions
- Logic Processor statements
- By-Phase functions
  - Pedestrian recall
  - Walk 2 enable
  - Vehicle extension 2 enable
  - Vehicle recall
  - Vehicle max recall
  - Max 2 enable
  - Max 3 enable
  - Conditional service inhibit
  - Phase omit

There shall be a minimum of 36 holiday or exception day programs, which override the normal day program. Holiday programs shall be capable of being set as floating (occurs on a specific day and week of the month) or fixed (occurs on a specific day of the year). It shall be possible to program a fixed holiday so that it automatically repeats in the following year.

It shall be possible to manually force any of the action plans to override the current action plan. The forced plan shall be entered from the keyboard and shall remain in effect until removed.

### (3) Time-Based Coordination

A minimum of 200 time-based schedule programs shall be available for the day-programs. There shall be up to 16-day plans with up to 50 events per day plan. These shall not have to be entered in any special sequence. It shall be possible to add and delete steps from a day-program without affecting any other day-program. Each of the program steps shall permit selection of the following functions:

- Day program assignment
- Start time
- Action plan

Selection of system override in an action plan shall allow the coordination pattern selected by the action plan to override the current telemetry or hardware system commanded coordination pattern.

When operating in the time-based coordination mode the synchronization point for all cycles shall be referenced to a user selected reference time (sync reference), last event, or last sync as selected from the keyboard. The sync reference time is that time at which all cycles shall be reset to zero.

If the sync reference time is selected, the synchronization point for the cycle selected by the current program step shall be computed using the present time, sync reference time, and cycle length. The synchronization point shall occur whenever the present time is such that an even number of cycle length periods has occurred since the sync reference time.

## W. Detectors

### (1) Detector Functions

The controller shall provide a minimum of 64 vehicle detector inputs. Each input shall be assignable to one or more phases and be configured with optional detector functions.

Extend and delay timing shall be provided for each detector.

Each detector shall be capable of operating in a lock or non-lock mode.

The controller shall also be capable of providing 16 pedestrian detector inputs. Each pedestrian detector shall be assignable to one or more phases.

## (2) Detector Cross Switching

The controller shall provide detector cross switching, permitting a vehicle detector to alternately place calls on assigned phases and assigned cross switch phases.

If the assigned phase is not green and the cross-switch phase is green, the detector shall place calls on the cross switch phase.

If the assigned phase is omitted for any reason, the detector shall place calls on the cross switch phase.

## (3) Detector Types

Each vehicle detector shall be user-programmable to operate as one of the following 8 detector types:

(a) **Type N (NTCIP)**: Supports all NTCIP detector functionality.

(b) **Type S (Standard)**: Supports all standard detector functionality except the call and extend options will always be enabled.

(c) **Type D (Disconnect queue/stop bar)**: This detector will disconnect from extending the phase during green if either there is a gap in the detector input OR the detectors disconnect time times out. The detector's input must be active before the phase turns green for this detector to operate.

(d) **Type P (Passage queue/stop bar)**: If the detector input is active when the phase turns green the detector passage time is reset as long as the input is active. When the detector input becomes inactive the detector passage timer begins running. If addition calls are received before the detector passage timer expires, the detector passage timer shall be reset until the input becomes inactive. Once the detector passage timer times out the detector is disconnected for the balance of the phase green.

(e) **Type C (Calling)**: This detector shall place calls on the phase only when the phase is NOT green.

(f) **Type R: (Red Extend)**: Actuation on a red extend detector during the red clearance of the assigned phase shall extend the red up to the red max phase time.

(g) **Type G (Green Delay/Extend)**: When the phase is red the detector input becoming active to the controller shall be delayed by the amount of time programmed in the delay time. When the phase is green, the detector input shall be extended after removed by the amount of time programmed in the detector extension timer.

(h) **Type B (Bike):** Activation shall cause the phase to use the Bike minimum green in place of the minimum green.

(4) System Detectors

Each detector input shall be capable of functioning as one of 16 system detectors.

Vehicle detectors shall be capable of being assigned to a minimum of 16 speed detectors. Speed shall be detected using both one and two detector configurations. Speed shall be computed using a keyboard entered average vehicle length and loop length for a one-detector configuration. When using two detectors, speed shall be calculated using a keyboard-entered distance between detectors and travel time between detectors.

(5) Logic Processor Commands

(a) Standard Logic Commands

One Hundred (100) logic processor commands shall be assessable from the front panel of the controller or through remote database management software.

Each logic command shall be allowed to be enabled or disabled by TOD control.

Each logic command shall consist of 10 "IF" statements, 5 "THEN" statements and 5 "ELSE" statements. At least 1 "IF" statement and 1 "THEN" statement are required to create a valid command.

"IF" statements shall be logically grouped using Boolean gating including:

- AND
- OR
- XOR
- NAND
- NOR
- 

When combined "IF" statements create one single "TRUE" result, the "THEN" statements shall be implemented.

When combined "IF" statements create one single "FALSE" result, the "ELSE" statements shall be implemented.

The "IF" statements shall monitor controller functions and timer including, but not limited to the following:

Phase/Overlap Green	Phase Calls
Phase/Overlap Yellow	Phase Omits
Phase/Overlap Red	Phase Force Offs
Phase/Overlap Active	Detector Fails
Detector Plan Number	Ring Timers
Detector Activity	Preemption Activity
Detector Volume	Preemption Dwell
Detector Occupancy	Preemption Exit
Coordination Timers	Stop Time
Coordination Holds	Manual Control
Phase Holds	Overlap timers (lagging/leading grn)
Phase Walks	Active Coordination Plan Number
Phase Don't Walks	TBC parameter
Phase Next	Input / Output Bits
Phase Checks	Logic Flags

The "THEN" and "ELSE" statements shall be used to control functions and events (on/off) in the controller, including, but not limited to, the following:

Set Logic Flags	Set Phase/OVL Greens
Set Phase calls	Set Phase/OVL Yellows
Set Input or Output Bits	Set Phase/OVL Reds
Set Delays	Set Walk / Ped Clear / Don't Walk
Set Load Switch circuits (R/Y/G)	Set Pedestrian Detectors
Set Vehicle Detectors	Set Holds
Omit Phase / Peds	Set Force-offs
Change Max Timers (max1, max 2)	Set Red Rest
Set Stop Time	Set Flash
Set Alarms	Set External Start
Set Recalls, CNA1, CNA2, etc	Set Manual Control Enable
Set Coordination Plan	Set Manual Advance
Set Free	Call Preempt
Call TSP	Call Phase
Set Action Plan	Set Timing Plan

(b) Extended Options Logic commands

An additional 100 logic commands (101-200) shall be available in the controller, but hidden from front panel access.

These commands shall be stored and used for easy implementation of common logic required at multiple intersections.

These commands shall only be assessable through computer software and shall be uploaded and downloaded to the controller using the same computer software.

Once downloaded to the controller, the logic shall be activated using an extended options file.

This file shall be downloaded to the controller and used to initialize the logic statements.

This file shall be used to create a custom menu on the controller. From this menu, the user shall be able to enable or disable any of the extended options hidden in the upper one hundred logic commands.

## X. System Communications

### (1) On-Street Master Communications

The controller shall be capable of communicating with an on-street system master. This capability shall be provided by a separate telemetry module, which shall be included in the controller when required by the plans and specifications.

The telemetry module shall receive system master commands and data transmissions. In addition, it shall transmit the controller status, database and system detector information to the system master.

If required by the plans and specifications, an external fiber optic modem or external radio or other means of communications connected by serial or Ethernet shall be provided instead of the telemetry module.

### (2) System Commands

The telemetry module shall allow the controller to receive, as a minimum, the following commands:

- Cycle, offset, and split (coordination pattern)
- System sync
- Special function commands (minimum of four)
- Free and flash mode commands
- Time and date
- Request for local status
- Recall to Max

All commands must occur more than once in any three-second period in order to be recognized.

All mode and special function commands shall be cleared after 20 minutes of loss of communication between controller and system master or central system.

The status of each of the following functions shall be transmitted to the system master or central system in response to a local status request:

- Green and yellow status for all phases and overlaps
- Walk and pedestrian clearance status for all phases
- Vehicle and pedestrian detector status
- Phase termination status
- Local time
- Coordination status
- Command source
- Sync or transitioning status of coordinator
- Conflict flash status
- Local flash status
- Preempt activity and calls
- Volume and occupancy data from a minimum of 16 system detectors
- Speed data from a minimum of two speed detectors
- Maintenance required (cabinet door open) status
- Status of two user-defined alarms

The status of each of the following parameters shall be calculated on a per-cycle basis and transmitted to the system master or central system for Split Monitor Reporting:

- Actual time spent in each phase
- Time of day at end of cycle
- Phases forced off during cycle
- Type of coordination operation
- Whether transitioning to new offset
- Cycle, offset, and split in effect during last cycle
- Flash status if operation is Free

Upload/Download Capability – The controller shall provide the capability to upload/download the entire intersection database. Phase assignments for overlaps and preemptors shall not be downloaded to preclude unsafe controller operation. Data transfer shall not require the intersection to be in flash.

### (3) Telemetry

Telemetry shall utilize TDM/FSK data transmission from 1200 baud to 9600 baud over two pairs of wires. These may be leased lines Type 3002, voice grade, unconditioned or dedicated cable. Optional external fiber optic or wireless communications capability shall also be available.

The nominal transmitter output level shall be 0 dbm into a 600-ohm load. The receiver sensitivity shall be -34 dbm and shall be adjustable from -40 to +6 dbm.

Parity and error checking shall be employed to assure transmission and reception of valid data. Indicators shall be provided on the telemetry module to show telemetry activity as follows: transmit, receive carrier, and valid data.

In the event of a telemetry failure, the controller shall revert to the non-interconnected coordination mode after it has self-synchronized for a number of cycles, which shall be selectable from 1-254. If the number of cycles is set to 255, the controller will self-synchronize until a synchronization pulse is detected.

#### (4) Communications Protocols

The controller shall have the capability of supporting communications with traffic management systems using industry standard protocols with the installation of appropriate optional software.

At a minimum, the controller shall have optional software to support the following protocols:

(a) Caltrans AB3418

(b) ECPIP

(c) NTCIP Level 2 as defined by Section 3.3.6 of NEMA TS2- 2003. NTCIP v02.06 capabilities shall include all NTCIP mandatory and optional objects. The controller vendor shall provide access to all controller data via vendor specific objects. These and all other objects supported by the controller shall be defined in a standard MIB file.

#### (5) Ethernet Communications

The controller shall have the capability of supporting Ethernet communications, using TCP/IP communications protocols.

This communications protocol shall utilize the controller's built-in switches and shall not require Ethernet-to-Serial converters.

#### (6) External Clock

The controller shall have the capability of communicating with an external clock like a GPS or WWV clock for setting its internal time of day clock.

The controller shall include a time reset input. This feature shall reset the TOD clock to 03:30 whenever the time reset input from the GPS or WWV clock is TRUE.

## Y. Diagnostics

### (1) General Diagnostics Features

The controller shall include both automatic and operator-initiated diagnostics. This capability shall be a standard feature and shall not require additional modules or software.

Automatic diagnostics shall verify memory and microprocessor operation each time power is reapplied to the controller. After power has been applied, diagnostics shall continually verify the operation of essential elements of the controller including communications.

### (2) Detector Diagnostics

TOD controlled detectors diagnostics shall be provided that allow testing vehicle and pedestrian detectors for no activity, maximum presence, and erratic output.

A minimum of four detector diagnostic plans shall be provided. These plans shall be selectable on a TOD basis. This shall allow varying the detector diagnostic intervals to correspond with changes in detector activity.

If a detector is diagnosed as failed, the associated phase shall be placed in one of the following keyboard selectable modes:

- (a) Detector fail recall from 1 to 255 seconds
- (b) Disable the detector from calling or extending.

Diagnostics for NEMA TS2 detectors connected to the controller using a Bus Interface Unit (BIU) shall also include detection of watchdog, open and shorted loop, and excessive inductance change failures.

## Z. Logging

The controller shall be capable of logging and reporting detector activity, detector failures, and the occurrence of selected events or alarms. Logs shall be capable of being printed or displayed on the front of the controller.

### (1) Detector Logging

The controller shall include a detector log buffer capable of logging volume, occupancy and average speed for selected vehicle and speed detectors.

The detector-logging interval shall be keyboard selectable as 5, 15, 30, or 60 minutes.

Detector logging shall be capable of being enabled or disabled by TOD.

### (2) Detector Failure Logging

The controller shall include a detector failure log buffer capable of storing a minimum of 100 time and date-stamped detector failure events. Once logged, detector failure events shall remain in the log until cleared or the log buffer capacity is exceeded at which time the oldest detector failure events shall be overwritten.

All detector diagnostic failures shall be recorded in the detector failure log including: no activity, maximum presence, erratic output, watchdog failure, open loop, shorted loop, and excessive inductance change. If a detector recovers after a diagnostic failure, a detector on-line event shall be stored in the detector failure log.

Detector failure logging shall be capable of being disabled.

### (3) Event Logging

The controller shall include an event log buffer capable of storing a minimum of 200 time and date-stamped events or alarms. Once logged, events shall remain in the buffer until cleared or the log buffer capacity is exceeded at which time the oldest events shall be overwritten.

At a minimum, the following events shall be logged: communication failures, coordination faults, MMU and local flash status, preempt, power ON/OFF, low battery, and status of a minimum of two alarm inputs. An on-line event shall be logged when an event or alarm returns to normal status.

If security is enabled, an event shall be logged when a user enters a data change. This event shall include the user's ID. It is necessary to log the first change only and not every change. Also, an entry shall be recorded when a user logs in and out of the controller.

Event logging shall be capable of being enabled or disabled for each category of event or alarm.

## AA. Optional Software Modules

The optional software modules shall not be considered as base features of the controller. These features shall be enabled through a special function data key, which when inserted in the controller shall unlock the software features and allow the controller to activate the functionality.

### (1) Transit Signal Priority (TSP)

Six (6) TSP plans shall be available to allow priority control of identified transit vehicles.

The transit vehicles shall be identified through special inputs into the controller. The inputs shall be of the following types:

(a) Check-In and Check-Out – These shall be two momentary inputs providing a service request for the transit vehicle. TSP shall be given to the vehicle from the time the Check-In signal is received to the moment the Check-Out signal is received, or the max TSP timer expires, whichever comes first.

(b) Constant Call – This shall be one continuous input providing a service request for the transit vehicle. TSP shall be given to the vehicle from the time the signal is received to the moment the call is terminated, or the max TSP timer expires, whichever comes first.

The TSP algorithm shall provide priority service to the transit vehicle without terminating coordination plans or skipping phases.

### **3. Exceptions**

This specification does not include any proprietary items or devices which would preclude any equipment manufacturer from reproducing equipment to meet these specifications. All technical tolerances, ratings, and technically specified criteria contained within these specifications are currently being met by commercially available equipment. The fact that a manufacturer chooses not to produce equipment to meet these specifications, providing the above criteria are met, will not be considered sufficient cause to adjudge these specifications as restrictive.

### **4. Guarantee**

As a minimum, each controller shall be guaranteed against defects in materials and workmanship for a period of 18 months from date of shipment. Details of the manufacturer's guarantees and warranties shall be supplied with the bid. The warranty offered by the manufacturer must be assigned to the Road Commission for Oakland County.

### **5. Packing and Marking**

Except as otherwise specified in the request for quotation, each controller shall be packed separately in such a manner that there will be no injury or defacement to the controller during transportation to the point of destination. Each carton shall be legibly marked with the controller description, purchase order number, and vendor's name.

### **6. Sampling and Testing**

One or more controller may be selected at random from the shipment and tested in accordance with Road Commission methods for compliance with the requirements of these specifications. If so notified by the Road Commission, the vendor shall complete one controller for preliminary testing and inspection to determine compliance with these specifications. If this preliminary sample controller conforms to these specifications, the vendor will be notified in writing and shall furnish the remainder of the order, which will be subject to testing in accordance with Road Commission methods. If the preliminary sample does not conform to these specifications, either the order will be canceled or the vendor will be notified, in writing, of all deficiencies

so that the necessary changes or corrections to all cabinets on the purchase order may be made. The entire order will then be subject to testing in accordance with Road Commission methods.

**7. Patented Processes and Materials**

The vendor shall defend any and all patent infringement suits resulting from the use of any design, device, material or process, or portion or phase thereof, employed in the manufacture or use of said controllers in accordance with the department's plans and specifications, and he shall save harmless and indemnify the Road Commission on account of any and all such suits or claims for royalties, damages, or costs.

**8. Delivery**

The bidder shall state how soon delivery can be made. The price quoted shall include the cost of delivery to the Road Commission for Oakland County, 2420 Pontiac Lake Road, Waterford, MI 48328.

**9. Acceptance**

Provide General Certification per the MDOT's *Materials Quality Assurance Procedures Manual* to the Engineer that the materials meet the requirements specified herein.

**c. Construction**

All work must be in accordance with the contract documents.

**d. Measurement and Payment**

The completed work, as described, will be measured and paid for at the contract unit price using the following pay item(s).

<b>Pay Item</b>	<b>Pay Unit</b>
Controller, Digital Type, Delivered, Modified, RCOC .....	Each

**Controller, Digital Type, Delivered, Modified, RCOC (Ea)** will be measured as a unit. The unit shall be as indicated on plans. The contract unit price each shall be payment in full for furnishing and delivering to RCOC for timing and set-up the controller(s) as specified herein and as shown on plans.